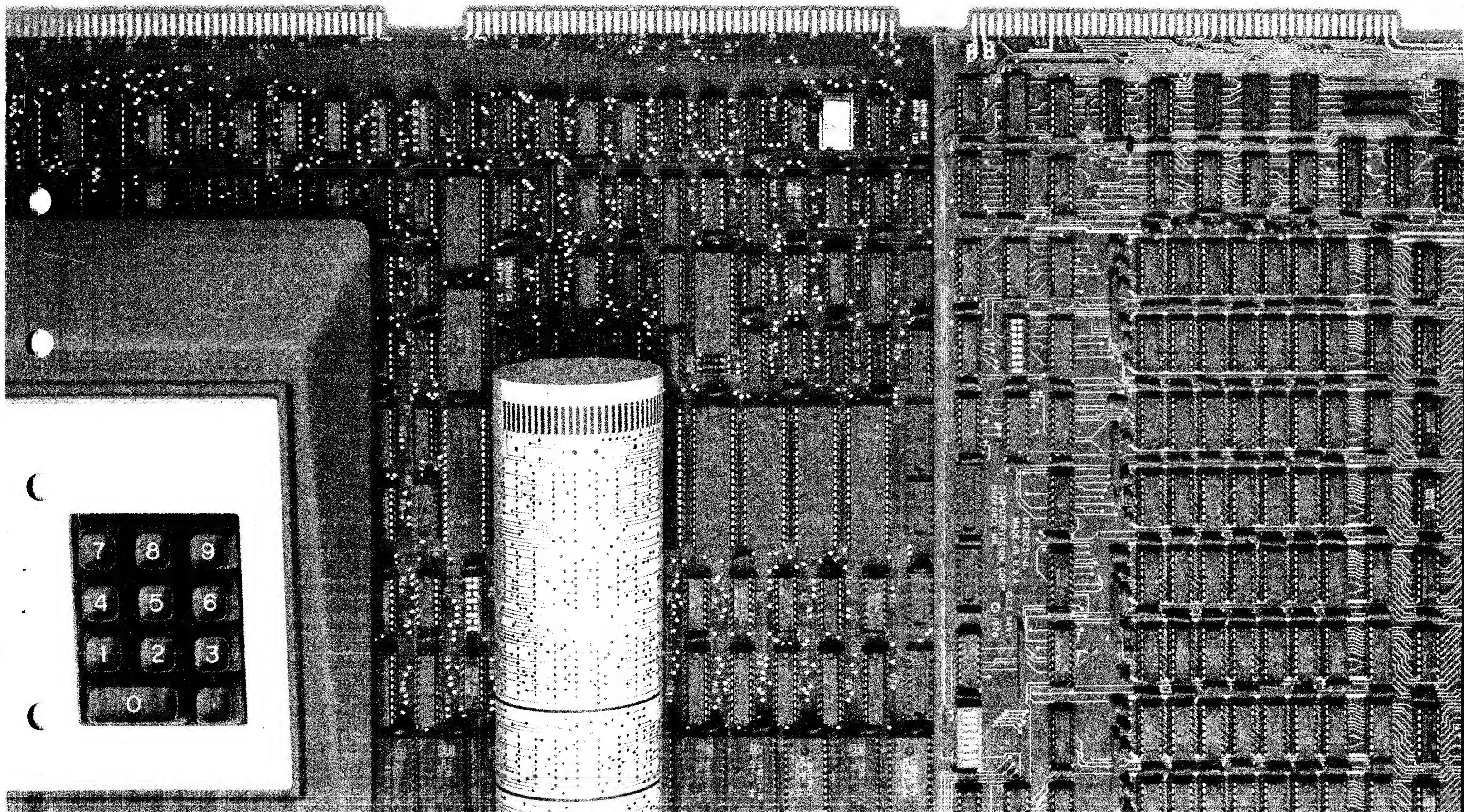


Disc Processing Unit (DPU)  
Logic Diagrams

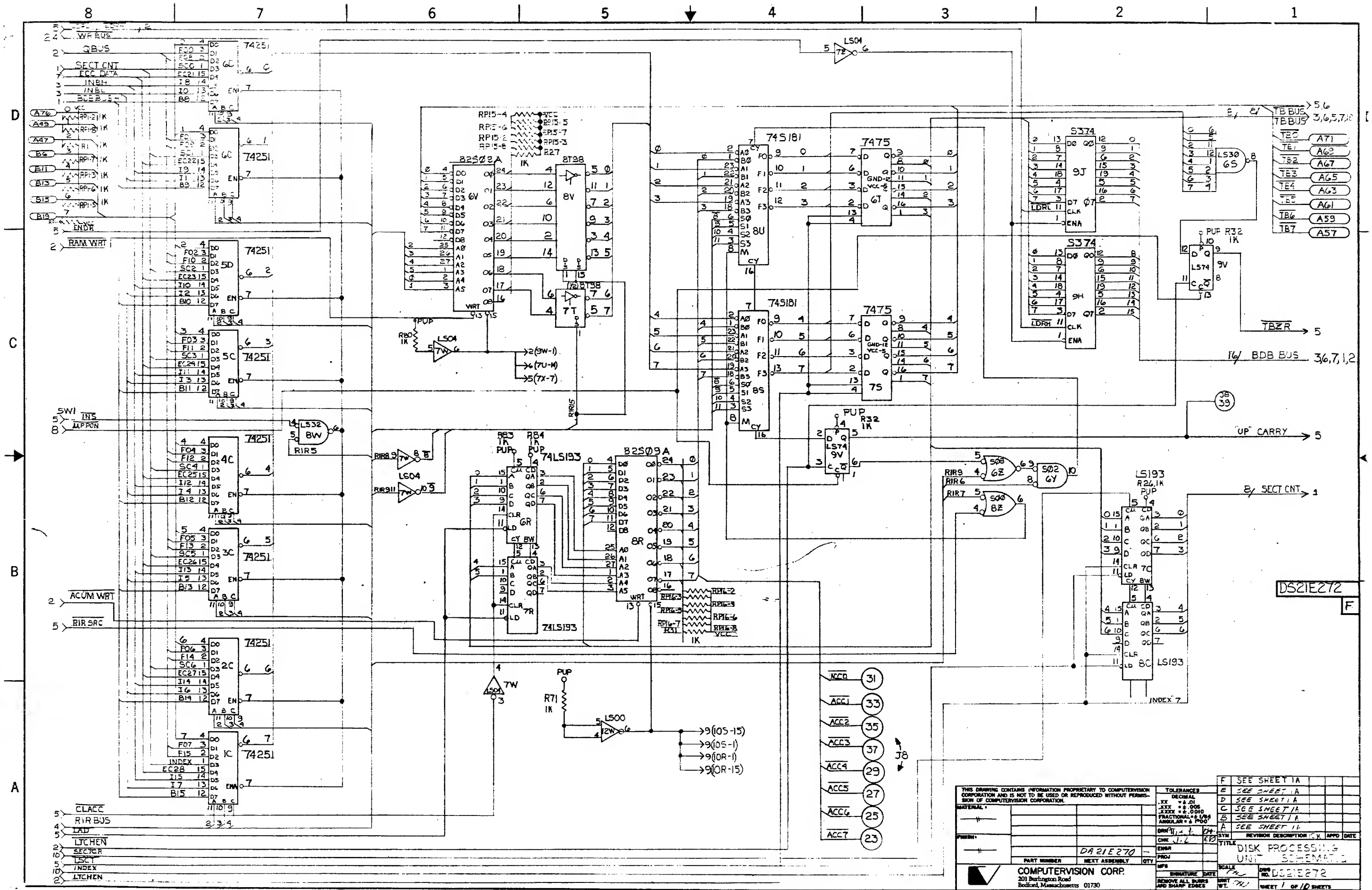


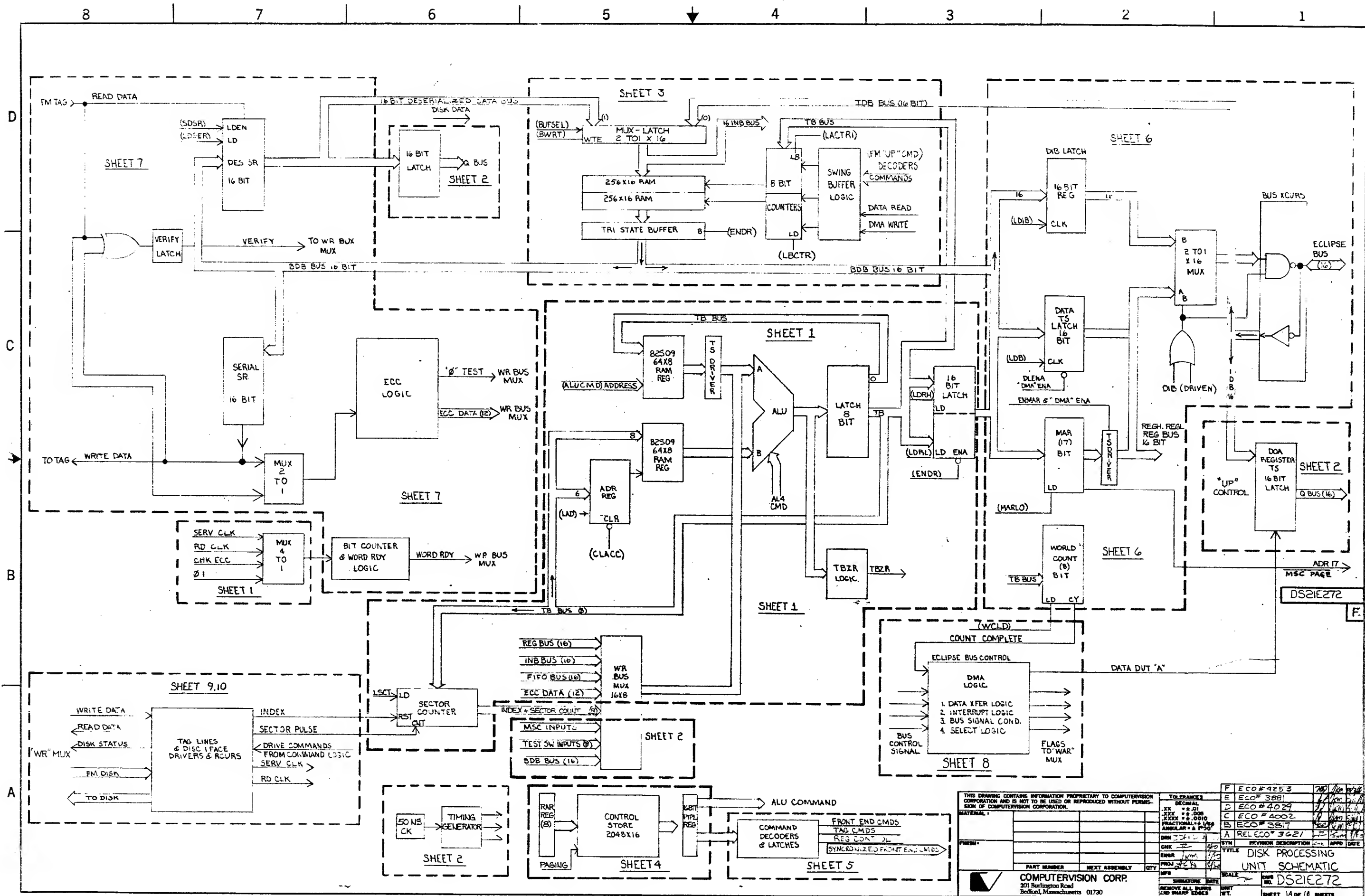


**Disc Processing Unit (DPU)**  
Logic Diagrams

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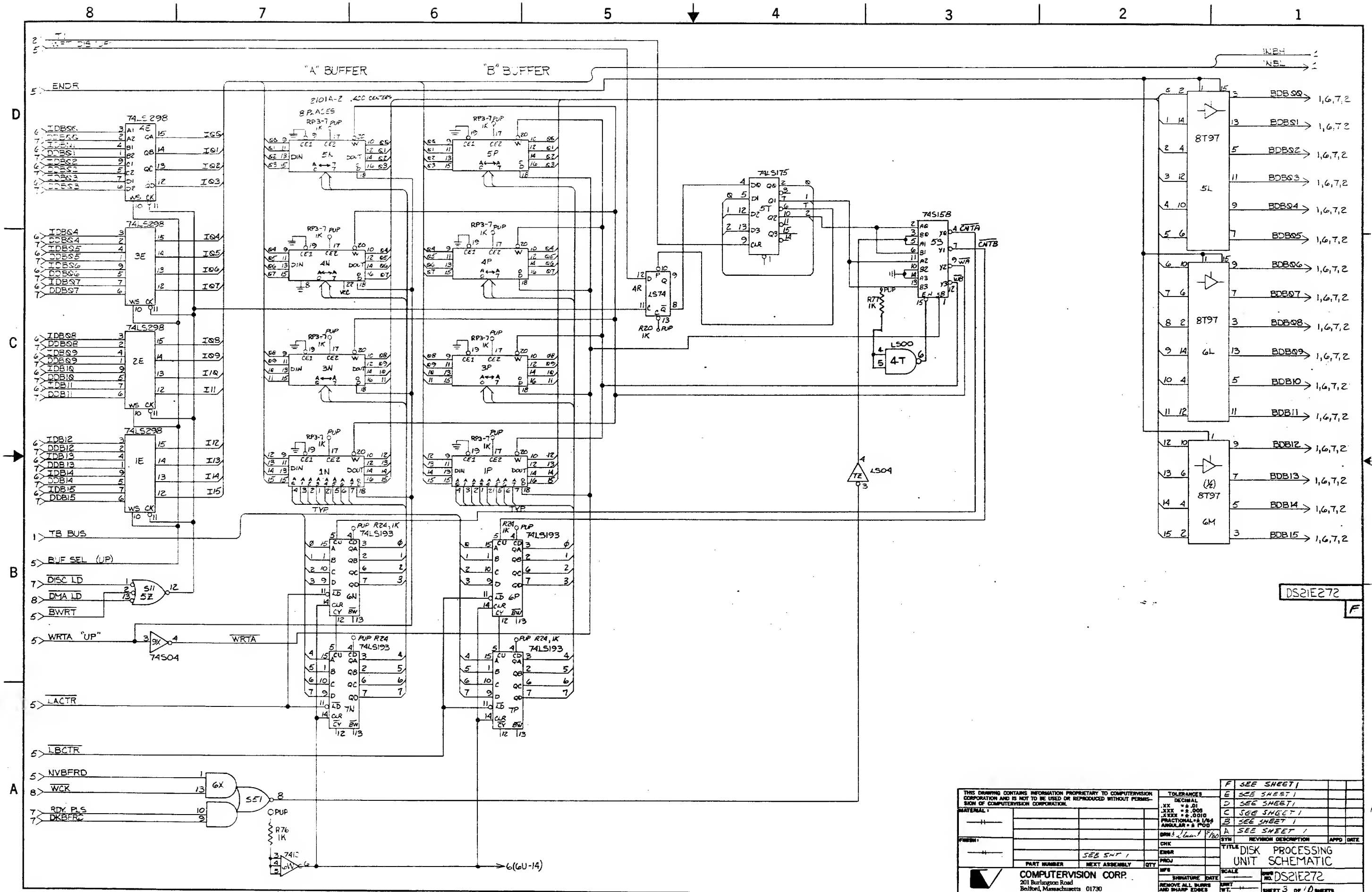
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PART NUMBER		NEXT ASSEMBLY		GTY		TITLE DISK PROCESSING UNIT SCHEMATIC	
COMPUTERSERV CORP.		201 Burlington Road Bedford, Massachusetts 01730		SCALE		DRAW NO. DS21E272	
SIGNATURE		DATE		REVISION		SHEET 1A OF 18 SHEETS	

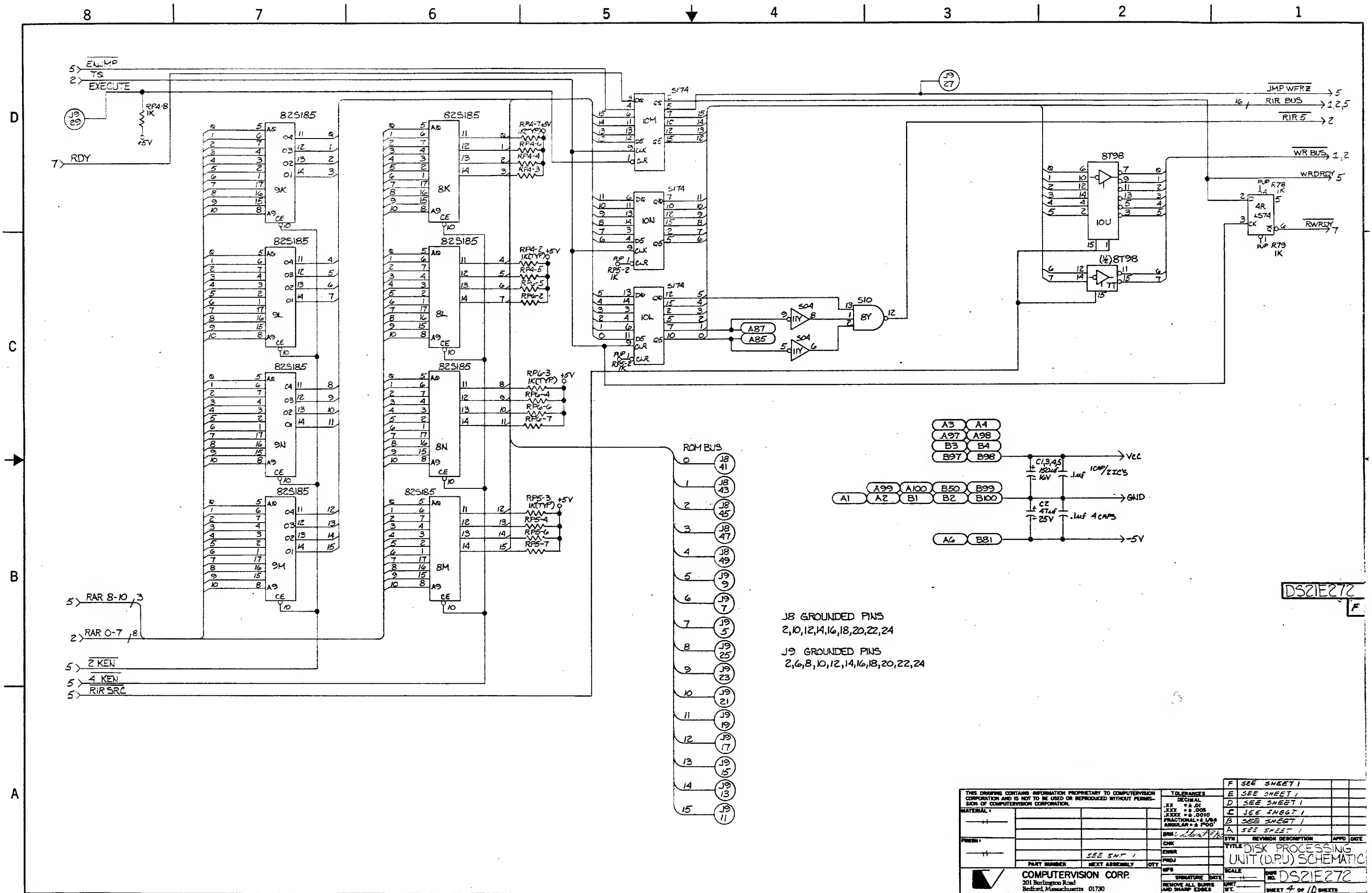




DS21E272

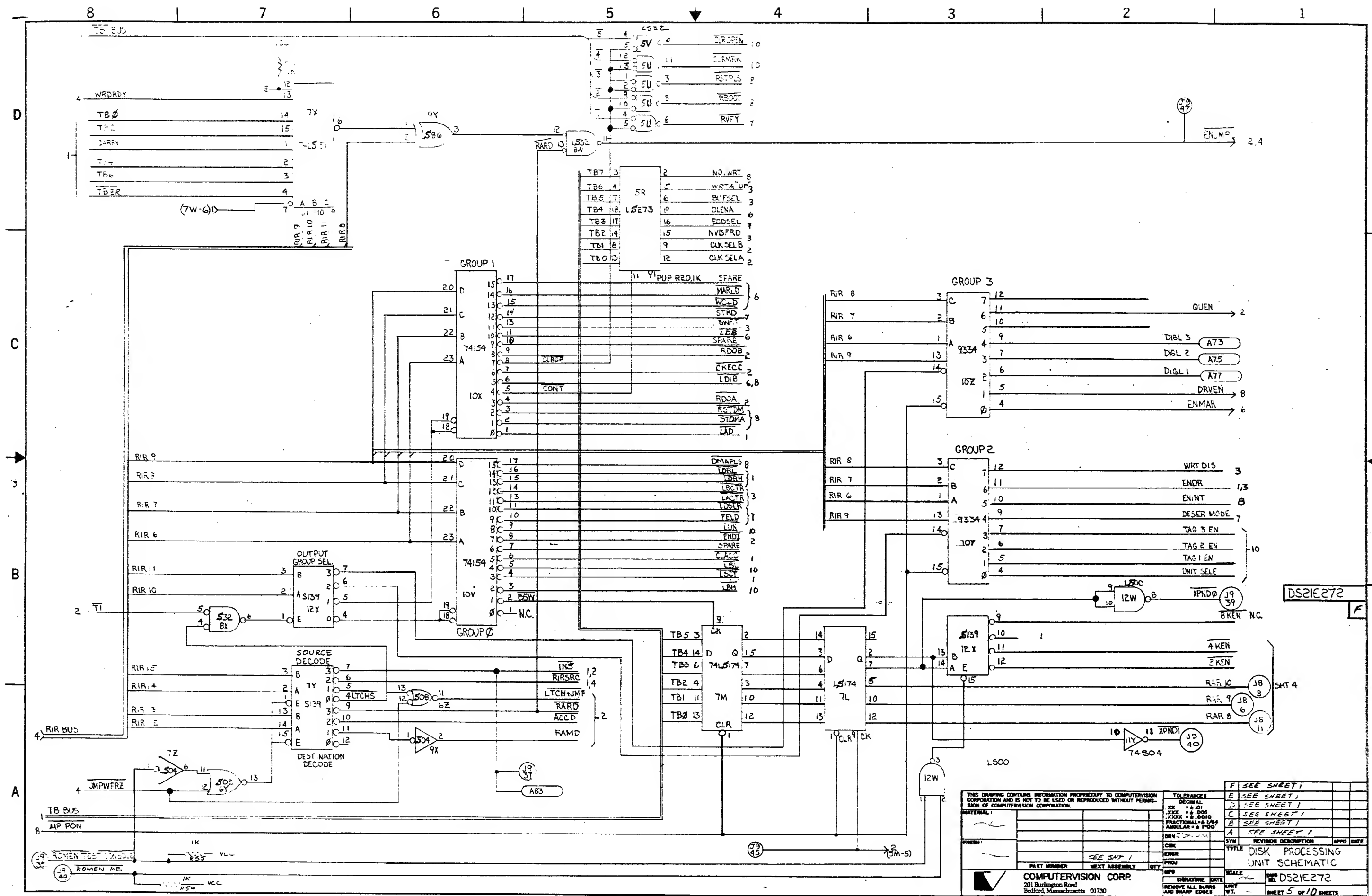
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				XXX ±0.005		C SEE SHEET 1	
				XXX ±0.0010		B SEE SHEET 1	
				FRACTIONAL ±1/64		A SEE SHEET 1	
				ANGULAR ±1°00'			
				DIM ±0.001			
				CHG		SYN REVISION DESCRIPTION	
				ENGR		APPD DATE	
				PROJ		TITLE DISK PROCESSING	
				MFR		UNIT SCHEMATIC	
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				SIGNATURE DATE		SHEET 3 OF 10 SHEETS	
				REMOVE ALL DIMS AND SHARP EDGES			
				WT.			

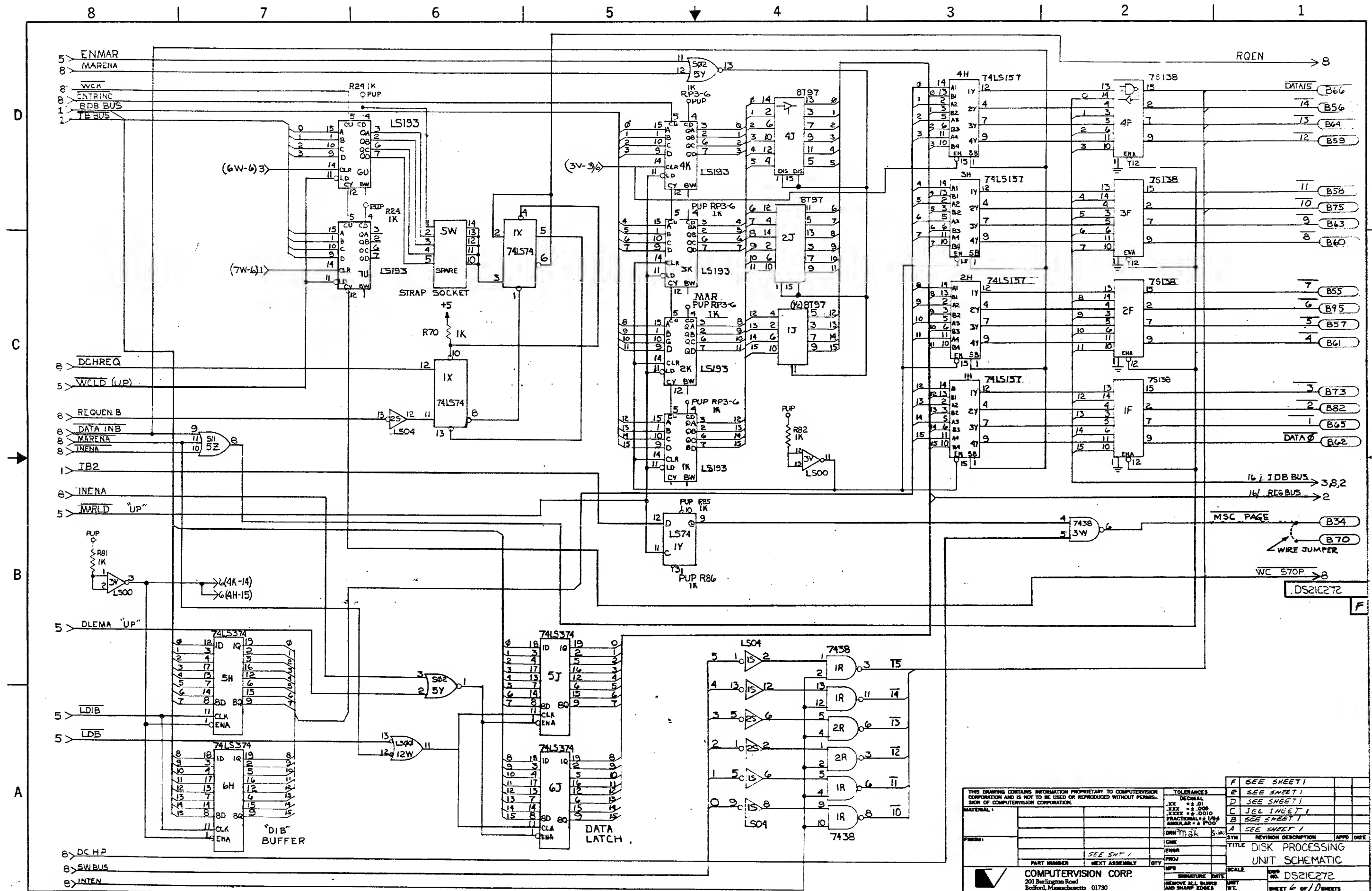


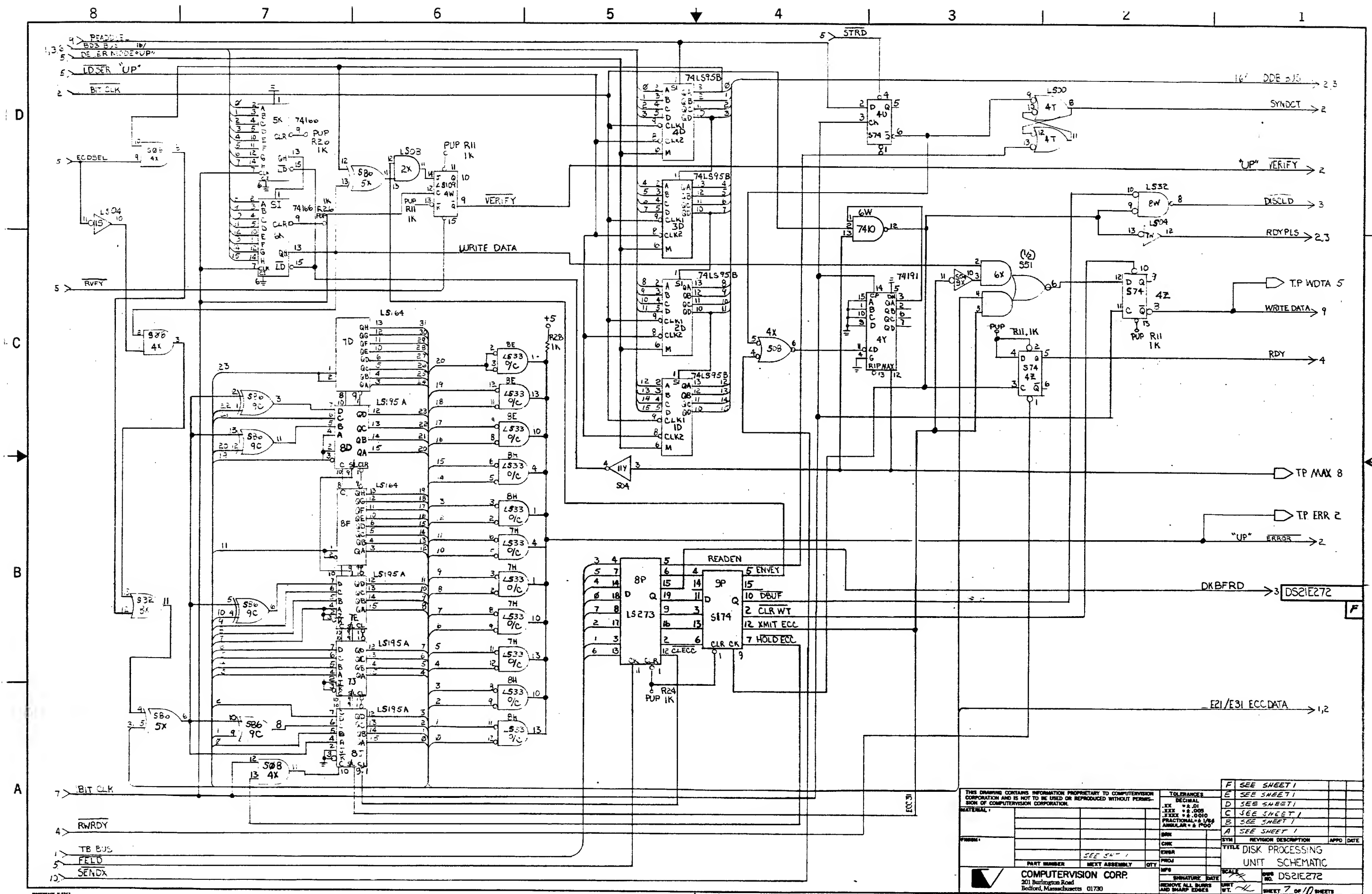
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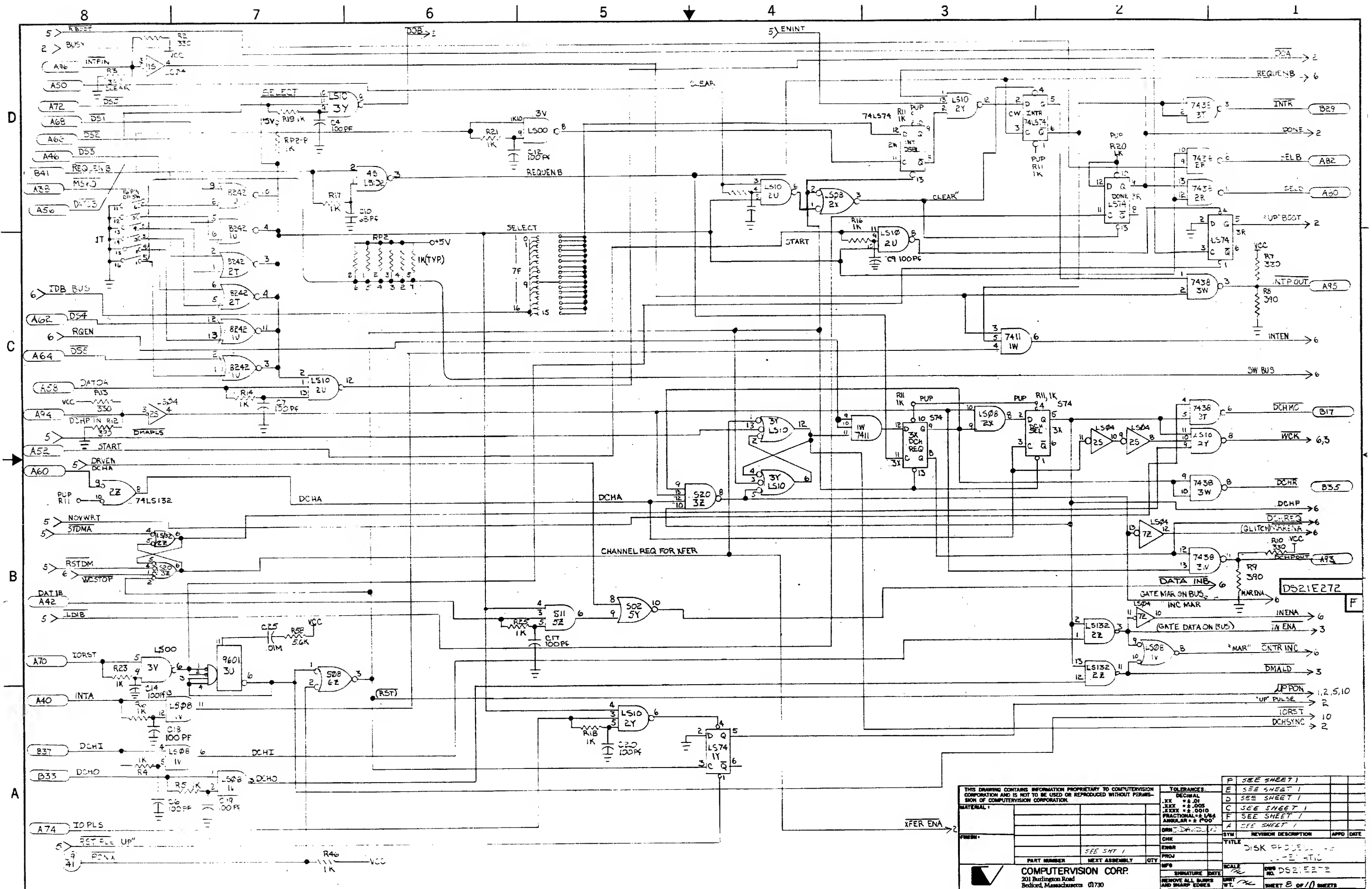
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MATERIAL		CHK		SYN	
FINISH		ENGR		REV	
PART NUMBER		NEXT ASSEMBLY		QTY	
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		SHEET 4 OF 10 SHEETS			

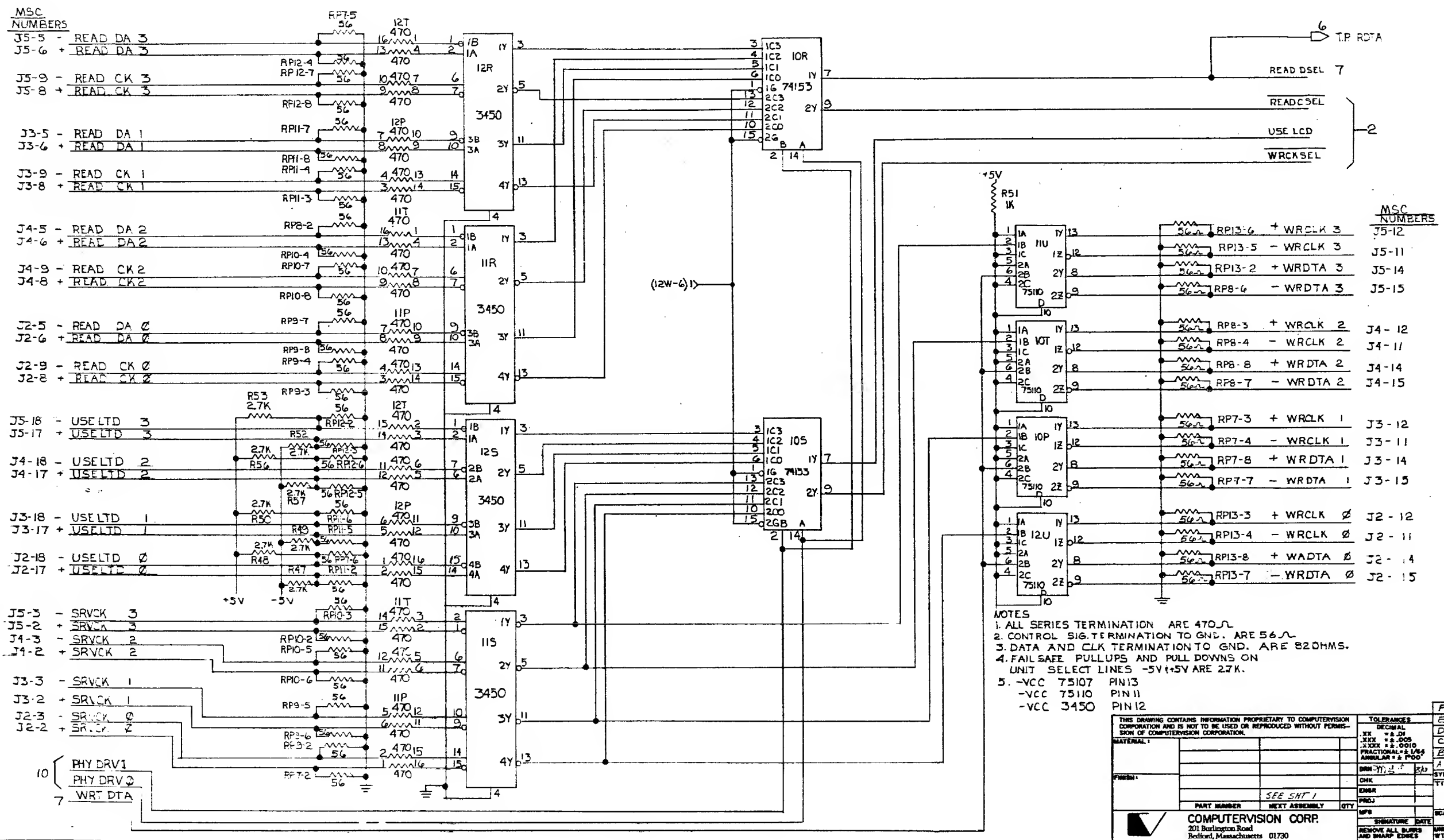
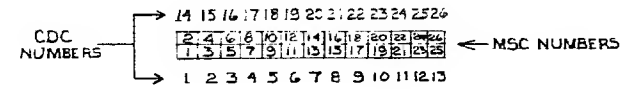








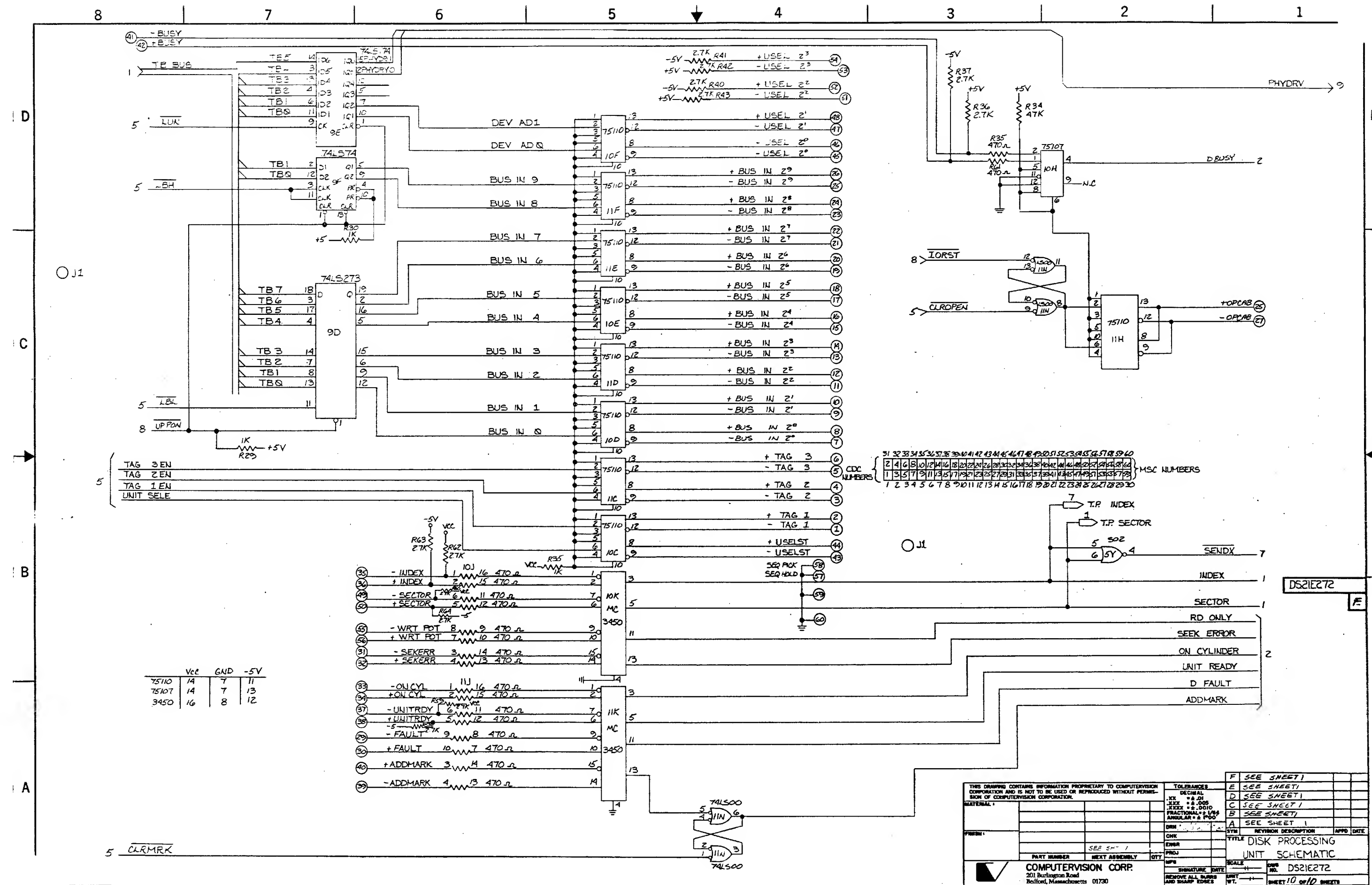




- NOTES
1. ALL SERIES TERMINATION ARE 470Ω.
  2. CONTROL SIG. TERMINATION TO GND. ARE 56Ω.
  3. DATA AND CLK TERMINATION TO GND. ARE 82ΩHMS.
  4. FAILSAFE PULLUPS AND PULL DOWNS ON UNIT SELECT LINES -5V/+5V ARE 27K.
  5. -VCC 75107 PIN13  
-VCC 75110 PIN11  
-VCC 3450 PIN12

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MATERIAL:		CHK		SYN	
FRESH:		ENGR		APPD	
PART NUMBER		NEXT ASSEMBLY		QTY	
COMPUTERVISION CORP. 201 Burlington Road Bedford, Massachusetts 01730		SIGNATURE DATE		UNIT NO. DS21C272	
REMOVE ALL BURRS AND SHARP EDGES		UNIT NO.		SHEET 9 OF 10 SHEETS	





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